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Sheet	1	of			

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Application Number	10/036,910
Filing Date	12/21/2001
First Named Inventor	Mike DANIELSON et al.
Art Unit	2173
Examiner Name	Unassigned
Attorney Docket	CR0234M (72460)

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, paga(s), volume-issue number(s), publisher, city and/or country where published	T²		
OC.		Christoph HEER and Kay MIGGE; "VLSI Hardware accelerator for the MPEG-4 padding algorithm"; Part of the IS&T/SPIE Conference on Media Processors 1999, San Jose, California, January 1999; SPIE Vol. 3655; pgs. 113-119			
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